



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of**

Noboru Ichinose, et al.

**Serial No.:** 10/567,369

**Group Art Unit:** 2814

**Filed:** May 15, 2006

**Examiner:** Salerno, Sarah Kate

**For:** SEMICONDUCTOR LAYER WITH A GA<sub>2</sub>O<sub>3</sub> SYSTEM

Honorable Commissioner of Patents  
Alexandria, VA 22313 – 1450  
**Box Stop RCE**

**PRELIMINARY AMENDMENT**

Sir:

Concurrently with the RCE filed herewith and prior to further prosecution on the merits, please amend the above-identified application as follows:

**INTRODUCTORY COMMENTS**

Claims 1, 4, 6, 9-13, 15, and 21 are all the claims presently pending in the application.

Claims begin on page 2 of this paper. No new matter is added.

Remarks begin at page 4 of this paper.